

# picoSMDC Series

## Surface Mount



### Description

The picoSMDC series provides surface mount overcurrent protection for applications where space is at a premium and resettable protection is desired.

### Features & Benefits

- Broadest range of resettable devices available in industry
- Low resistance
- Small 0805 footprint
- Fast time-to-trip
- RoHS compliant, lead-free and halogen-free

### Applications

Mobile Electronics and Batteries

- Computer
- Portable electronics
- Multimedia
- Game machines
- Telephone and broadband
- Automotive
- Industrial controls
- Battery

### Additional Information



Resources



Accessories



Samples

### Agency Approvals

Agency	Agency File Number
	E74889
	78165
	72161782 0253253

### Electrical Characteristics

Part Number	Ordering Part Number	$I_H$	$I_T$	$V_{MAX}$	$I_{MAX}$	$P_{D MAX}$	Max Time-to-trip		$R_{MIN}$	$R_{1MAX}$
		(A)	(A)	(V <sub>DC</sub> )	(A)	(W)	(A)	(s)	( $\Omega$ )	( $\Omega$ )
<b>0805 Size</b>										
picoSMDC010S	RF1689-000	0.10	0.30	15	100	0.50	0.50	0.60	1.50	11.00
picoSMDC012S	RF1690-000	0.12	0.30	15	100	0.50	1.00	0.10	1.50	9.00
picoSMDC020S	RF1691-000	0.20	0.47	9	100	0.50	2.00	0.10	0.75	3.20
picoSMDC035S	RF1692-000	0.35	0.75	6	100	0.50	1.75	0.20	0.35	1.40
picoSMDC050S	RF1694-000	0.50	1.00	6	100	0.50	8.00	0.10	0.15	0.80
picoSMDC075S	RF1695-000	0.75	1.50	6	40	0.70	8.00	0.20	0.10	0.35
picoSMDC110S	RF1696-000	1.10	2.20	6	40	0.80	8.00	0.20	0.05	0.17

#### Notes:

- $I_H$  : Hold current: maximum current device will pass without interruption in 20°C still air.  
 $I_T$  : Trip current: minimum current that will switch the device from low resistance to high resistance in 20°C still air.  
 $V_{MAX}$  : Maximum continuous voltage device can withstand without damage at rated current.  
 $I_{MAX}$  : Maximum fault current device can withstand without damage at rated voltage.  
 $P_D$  : Power dissipated from device when in the tripped state in 20°C still air.  
 $R_{MIN}$  : Minimum resistance of device as supplied at 20°C unless otherwise specified.  
 $R_{1MAX}$  : Maximum resistance measured one hour post-trip or post-reflow at 20°C.

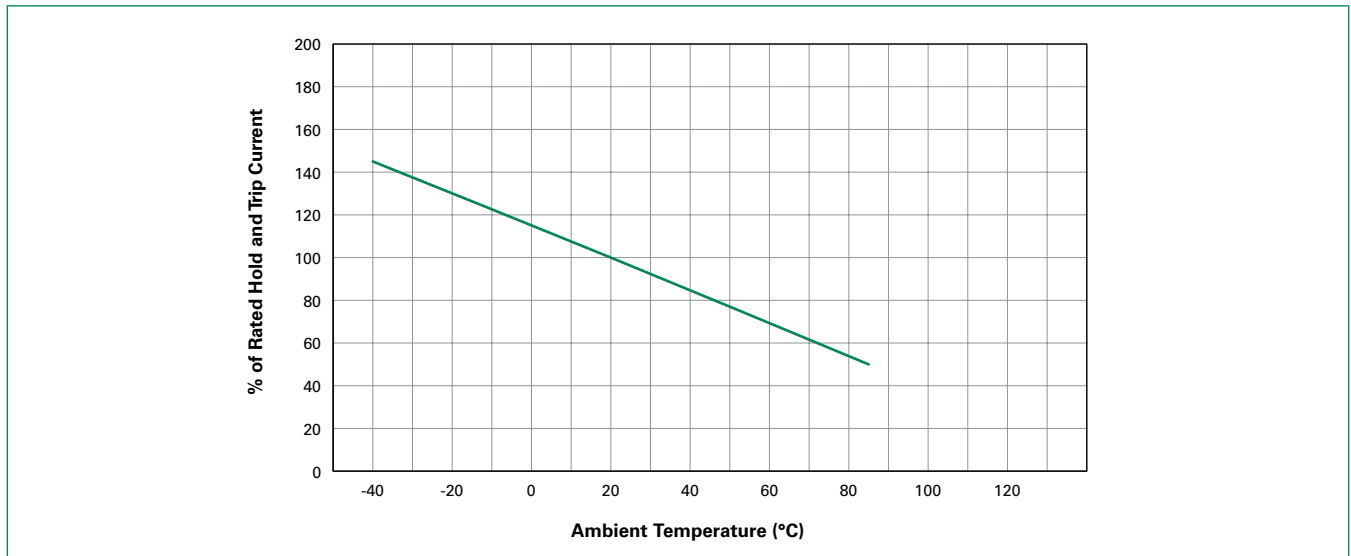
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### Temperature Rerating

Maximum Ambient Temperature											
Part Number	-40°C	-20°C	0°C	20°C	25°C	40°C	50°C	60°C	70°C	80°C	85°C
Hold Current (A)											
0805 Size											
picoSMDC010S	0.17	0.15	0.13	0.11	0.10	0.09	0.08	0.07	0.06	0.05	0.05
picoSMDC012S	0.20	0.17	0.15	0.13	0.12	0.10	0.09	0.08	0.07	0.06	0.05
picoSMDC020S	0.30	0.27	0.24	0.21	0.20	0.18	0.16	0.15	0.13	0.12	0.11
picoSMDC035S	0.55	0.49	0.44	0.37	0.35	0.31	0.28	0.26	0.23	0.20	0.18
picoSMDC050S	0.70	0.62	0.55	0.55	0.50	0.43	0.38	0.33	0.30	0.28	0.26
picoSMDC075S	1.13	1.01	0.90	0.78	0.75	0.67	0.61	0.55	0.49	0.43	0.40
picoSMDC110S	1.64	1.47	1.30	1.14	1.10	0.97	0.89	0.80	0.72	0.64	0.59

### Temperature Rerating Curve



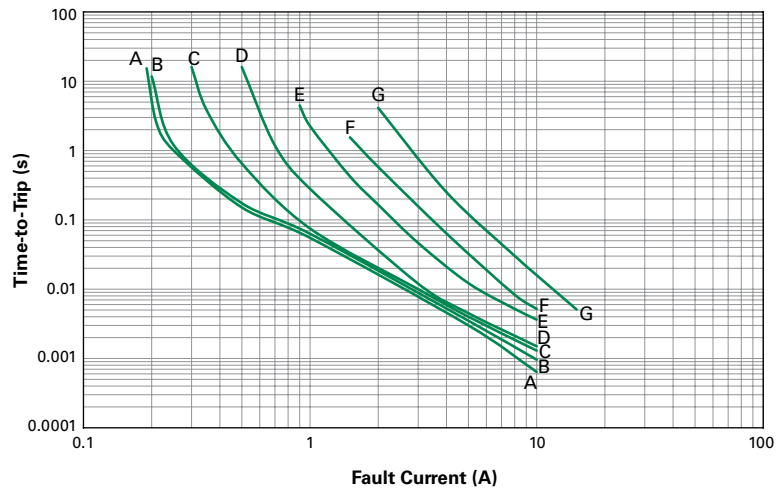
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### Typical Time-to-Trip Curves at 20°C

#### 0805 Series

- A = picoSMDC010S
- B = picoSMDC012S
- C = picoSMDC020S
- D = picoSMDC035S
- E = picoSMDC050S
- F = picoSMDC075S
- G = picoSMDC110S



**Note:** The average time current curves and Temperature Derating curve performance is affected by a number of variables, and these curves provided as guidance only. Customer must verify the performance in their application.

### Physical Specifications

<b>Terminal Pad Material</b>	100% Matte Tin with Nickel Underplate
<b>Soldering Characteristics</b>	ANSI/J-STD-002 Category 3
<b>Solder Heat Withstand</b>	per IEC-STD 68-2-20, Test Tb, Section 5, Method 1a
<b>Flammability Resistance</b>	per IEC 695-2-2 Needle Flame Test for 20 seconds

### Environmental Specifications

Test	Test Method	Conditions	Resistance Change
<b>Storage Life</b>	PS300, Section 5.3.2	60°C, 1000 hrs	±3% typ
		85°C, 1000 hrs	±3% typ
<b>Humidity Aging</b>	PS300, Section 5.3.1	85°C, 85% R.H., 100 hrs	±1.2% typ
<b>Thermal Shock</b>	MIL-STD-202, Method 107G	85°C, -40°C (20 Times)	-33% typ
		125°C, -55°C (10 Times)	-33% typ
<b>Vibration</b>	MIL-STD-883C	per MIL-STD-883C	No Change
<b>Solvent Resistance</b>	PS300, Section 5.2.2	Freon	No Change
		Trichloroethane	No Change
		Hydrocarbons	No Change

<b>Moisture Resistance Level</b>	Level 1, J-STD-020
<b>Storage Conditions</b>	40°C max, 70% RH max; devices should remain in original sealed bags prior to use. Devices may not meet specified values if these storage conditions are exceeded.

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### Dimension Figures

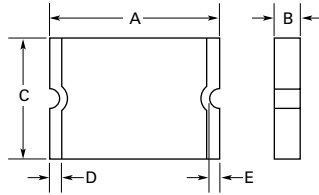


Figure 1

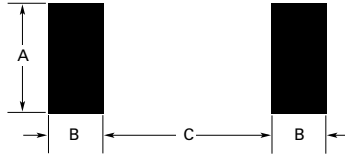
### Dimensions

Part Number	Dimensions in Millimeters (Inches)										Figure
	A		B		C		D		E		
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
<b>0805 Size</b>											
picoSMDC010S	2.00 (0.079)	2.20 (0.087)	0.60 (0.023)	1.00 (0.040)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC012S	2.00 (0.079)	2.20 (0.087)	0.44 (0.017)	0.68 (0.027)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC020S	2.00 (0.079)	2.20 (0.087)	0.44 (0.017)	0.68 (0.027)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC035S	2.00 (0.079)	2.20 (0.087)	0.44 (0.017)	0.68 (0.027)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC050S	2.00 (0.079)	2.20 (0.087)	0.63 (0.025)	0.93 (0.036)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC075S	2.00 (0.079)	2.20 (0.087)	0.63 (0.025)	0.93 (0.036)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoSMDC110S	2.00 (0.079)	2.20 (0.087)	0.80 (0.031)	1.20 (0.047)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1

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### Recommended Pad Layout



### Packaging and Marking Information

Part Number	Tape and Reel Quantity	Standard Package	Part Marking	Recommended Pad Layout Figures [mm (in)]			Agency Recognition
				Dimension A (Nom)	Dimension B (Nom)	Dimension C (Nom)	
<b>0805 Size</b>							
picoSMDC010S	3,000	15,000	C	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC012S	4,000	20,000	F	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC020S	4,000	20,000	H	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC035S	4,000	20,000	I	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC050S	3,000	15,000	K	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC075S	3,000	15,000	M	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, CSA, TÜV
picoSMDC110S	3,000	15,000	S	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)	UL, TÜV

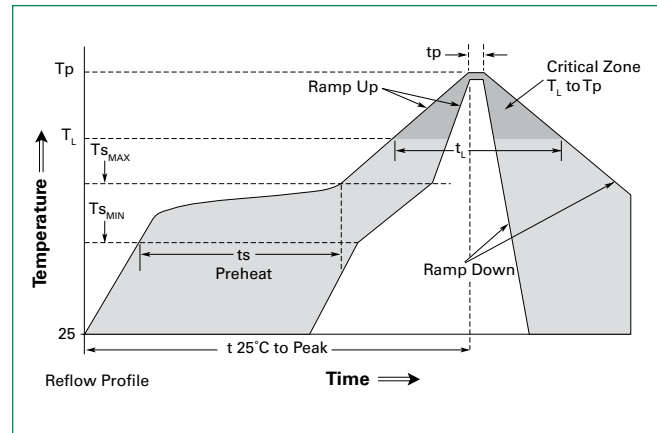
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### Solder Reflow Recommendations

<b>Profile Feature</b>	Pb-Free Assembly
<b>Average ramp up rate (<math>T_{S\_MAX}</math> to <math>T_p</math>)</b>	3°C/s max
<b>Preheat</b>	
• Temperature min ( $T_{S\_MIN}$ )	150°C
• Temperature max ( $T_{S\_MAX}$ )	200°C
• Time ( $t_{S\_MIN}$ to $t_{S\_MAX}$ )	60-120 s
<b>Time maintained above:</b>	
• Temperature ( $T_L$ )	217°C
• Time ( $t_L$ )	60-150 s
<b>Peak/Classification temperature (<math>T_p</math>)</b>	260°C
<b>Time within 5°C of actual peak temperature</b>	
Time ( $t_p$ )	30 s max
<b>Ramp down rate</b>	3°C/s max
<b>Time 25°C to peak temperature</b>	8 min max

**Note:** All temperatures refer to topside of the package, measured on the package body surface.



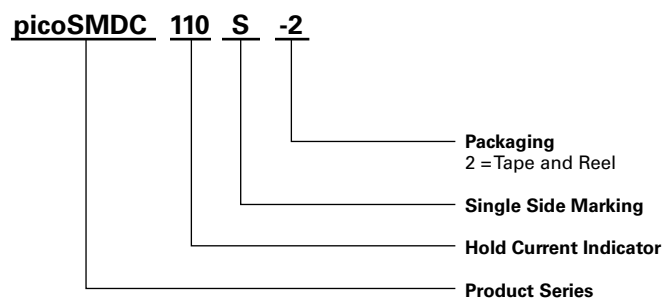
### Solder Reflow

- Recommended reflow method: IR, hot air, nitrogen.
- Recommended maximum paste thickness: 0.25mm (0.010in)
- Devices can be cleaned using standard methods and aqueous solvents.
- Experience has shown the optimum conditions for forming acceptable solder fillets occur when a reasonable amount of solder paste is placed underneath each device's termination. As such, we request that customers comply with our recommended solder pad layouts.
- Customer should validate that the solder paste amount and reflow recommendations meet its application.
- We request that customer board layouts refrain from placing raised features (e.g. vias, nomenclature, traces, etc.) underneath PolySwitch devices. It is possible that raised features could negatively impact solderability performance of our devices.

### Rework

- Standard industry practices. (Please also avoid direct contact to the device.)

### Part Ordering Number System



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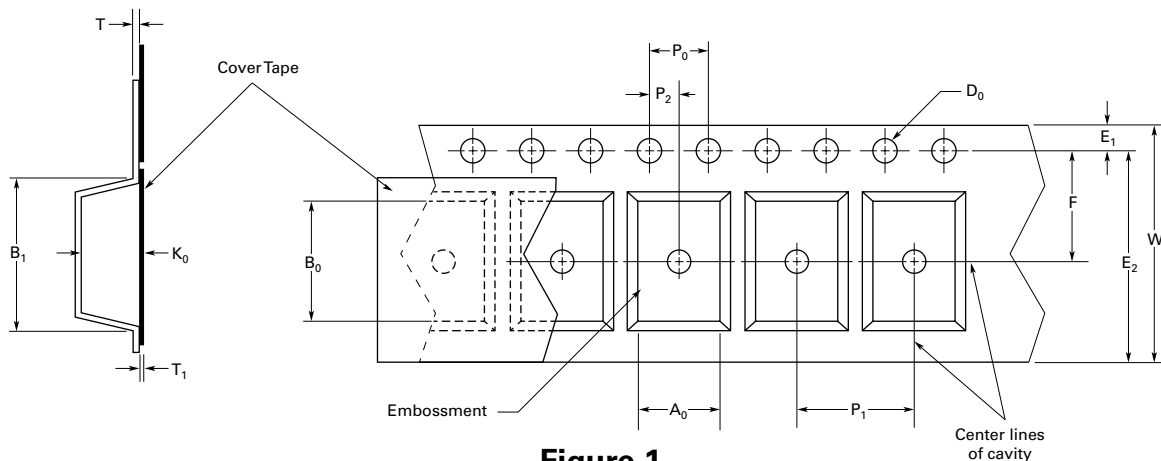
### Tape and Reel Specifications

Description	picoSMDC EIA 481-1 (mm)		
	picoSMDC012S picoSMDC020S picoSMDC035S	picoSMDC010S picoSMDC050S picoSMDC075S	picoSMDC110S
<b>W</b>	8.0 ± 0.30	8.0 ± 0.30	8.0 ± 0.30
<b>P<sub>0</sub></b>	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10
<b>P<sub>1</sub></b>	4.0 ± 0.10	4.0 ± 0.10	4.0 ± 0.10
<b>P<sub>2</sub></b>	2.0 ± 0.10	2.0 ± 0.05	2.0 ± 0.05
<b>A<sub>0</sub></b>	1.70 ± 0.10	1.70 ± 0.10	1.70 ± 0.10
<b>B<sub>0</sub></b>	2.45 ± 0.10	2.45 ± 0.10	2.45 ± 0.10
<b>B<sub>1</sub> max</b>	4.35	4.35	4.35
<b>D<sub>0</sub></b>	1.55 ± 0.05	1.55 ± 0.05	1.55 ± 0.05
<b>F</b>	3.50 ± 0.05	3.50 ± 0.05	3.50 ± 0.05
<b>E<sub>1</sub></b>	1.75 ± 0.10	1.75 ± 0.10	1.75 ± 0.10
<b>E<sub>2</sub> min</b>	6.25	6.25	6.25
<b>T max</b>	0.3	0.3	0.3
<b>T<sub>1</sub> max</b>	0.1	0.1	0.1
<b>K<sub>0</sub></b>	0.86 ± 0.1	1.12 ± 0.1	1.35 ± 0.1
<b>A max</b>	185	185	185
<b>N min</b>	50	50	50
<b>W<sub>1</sub></b>	8.4 + 1.5/-0.00	8.4 + 1.5/-0.00	8.4 + 1.5/-0.00
<b>W<sub>2</sub> max</b>	14.4	14.4	14.4

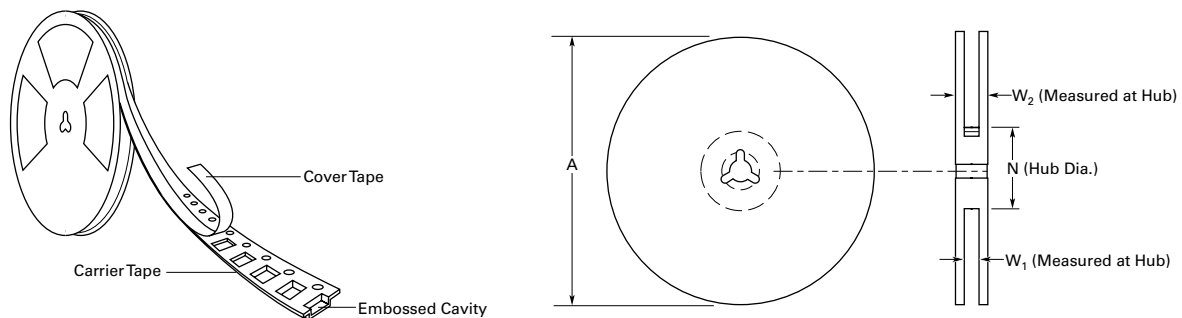
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### Tape and Reel Diagrams



**Figure 1**



**Figure 2**

#### Warning

- Users should independently evaluate the suitability of and test each product selected for their own application.
- Operation beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- These devices are intended for protection against damage caused by occasional overcurrent or overtemperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Contamination of the PPTC material with certain silicone-based oils or some aggressive solvents can adversely impact the performance of the devices.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- PPTC devices are not recommended for installation in applications where the device is constrained such that its PTC properties are inhibited, for example in rigid potting materials or in rigid housings, which lack adequate clearance to accommodate device expansion.
- Operation in circuits with a large inductance can generate a circuit voltage ( $Ldi/dt$ ) above the rated voltage of the device.

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